

BUR9-2000-0086-US1

XN  
JC825 U.S. PRO  
09/708494  
11/09/00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of

Pascal A. NSAME

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: **SYSTEM-ON-A-CHIP STRUCTURE HAVING A MULTIPLE CHANNEL  
BUS BRIDGE**

Assistant Commissioner of Patents  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

BUR9-2000-0086-US1

Please charge any deficiencies in fees and credit any overpayment of fees to  
Attorney's Deposit Account No. 09-0456.

Respectfully submitted,



Frederick W. Gibb, III  
Registration No. 37,629

McGinn & Gibb, P.C.  
Intellectual Property Law  
8321 Old Courthouse Road, Suite 200  
Vienna, Virginia 22209  
(703) 761-4100  
Customer No. 21254

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) <b>BUR9-2000-0071-US1</b>		Application Number <b>Not Yet Assigned</b>	
				Applicant(s) <b>Pascal A. Nsame</b>		Filing Date <b>Concurrently Herewith</b>	
<b>U.S. PATENT DOCUMENTS</b>							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		<b>5,438,681</b>	<b>08/01/95</b>	<b>Mensch, Jr.</b>			
		<b>5,455,525</b>	<b>10/03/95</b>	<b>Ho et al.</b>			
		<b>5,761,516</b>	<b>06/02/98</b>	<b>Rostoker et al.</b>			
		<b>5,842,031</b>	<b>11/24/98</b>	<b>Barker et al.</b>			
		<b>6,035,364</b>	<b>03/07/00</b>	<b>Lambrecht et al.</b>			
		<b>5,838,684</b>	<b>11/17/98</b>	<b>Wicki et al.</b>			
<b>FOREIGN PATENT DOCUMENTS</b>							
REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
		<b>Pascal Nsame, Yvon Savaria, VIRTUALISING ON-CHIP BUS INTERFACES FOR IMPROVED EMBEDDED PROCESSOR SYSTEM PERFORMANCE, Institut National Polytechnique de Grenoble, pp. 138-143 (December 14-15, 1999).</b>					
EXAMINER				DATE CONSIDERED			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							